

FIG. 1A

A 4x4 grid of numbers from 0 to 15. The grid is labeled with reference numerals 110 and 105. Reference numeral 110 is positioned above the first column, and reference numeral 105 is positioned to the right of the fourth row.

0	1	2	3
4	5	6	7
8	9	10	11
12	13	14	15

FIG. 1B

A 4x4 grid of numbers from 0 to 15. The grid is labeled with reference numerals 155 and 150. Reference numeral 155 is positioned above the first column, and reference numeral 150 is positioned to the right of the fourth row.

0	1	2	3
4	5	6	7
8	9	10	11
12	13	14	15

FIG. 2

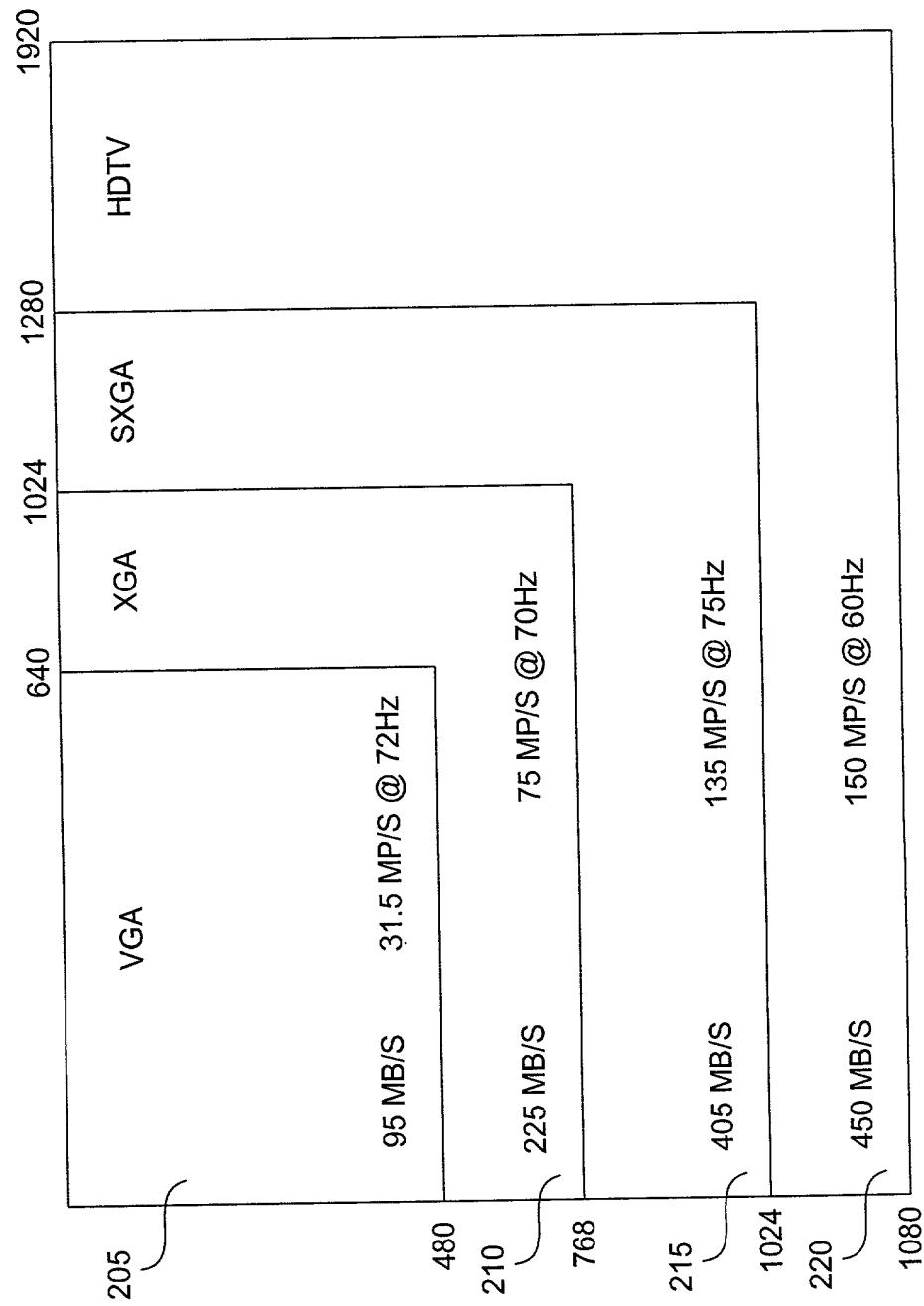


FIG. 3A

310

305

0	1	2	3	4	5	6	7
8	9	10	11	12	13	14	15
16	17	18	19	20	21	22	23
24	25	26	27	28	29	30	31

100054541 "00116022

350

FIG. 3B

350

0	2	4	6
8	10	12	14
16	18	20	22
24	26	28	30

FIG. 3C

375

1	3	5	7
9	11	13	15
17	19	21	23
25	27	29	31

FIG. 4

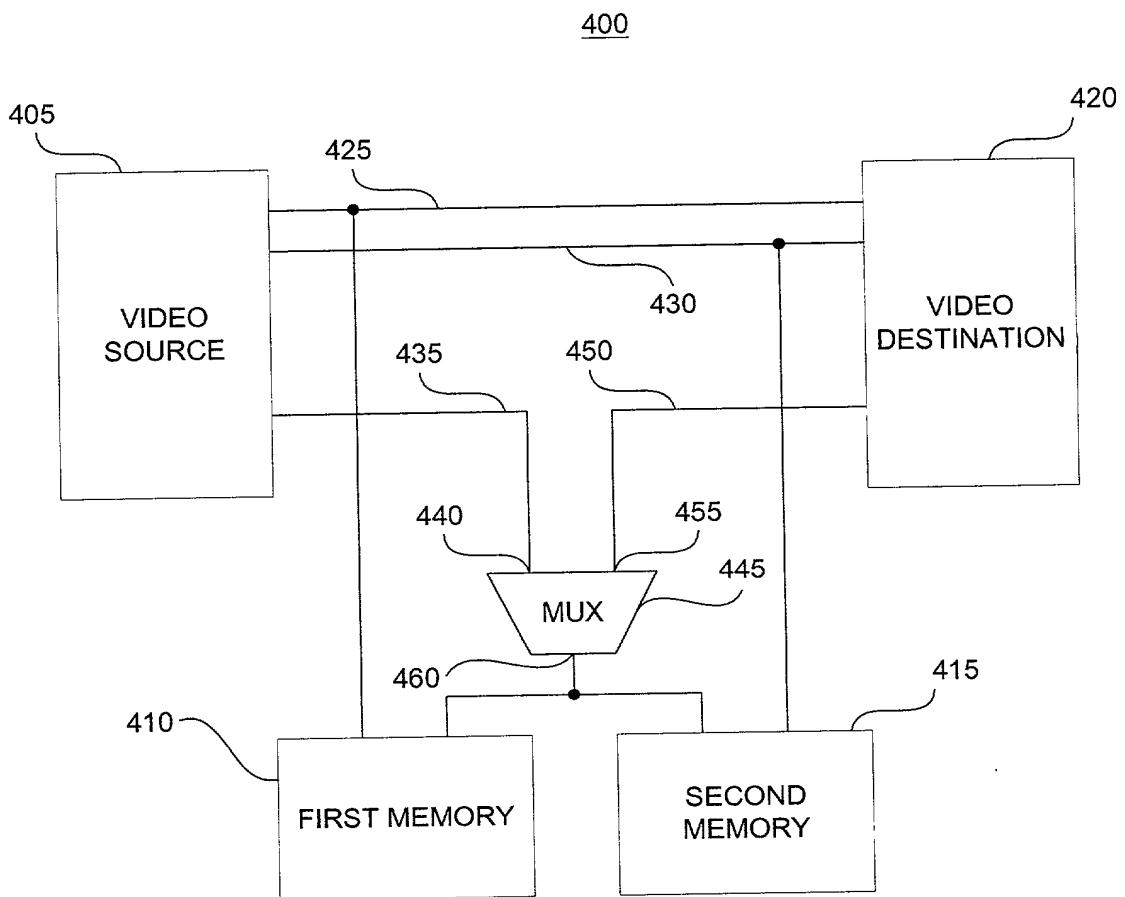
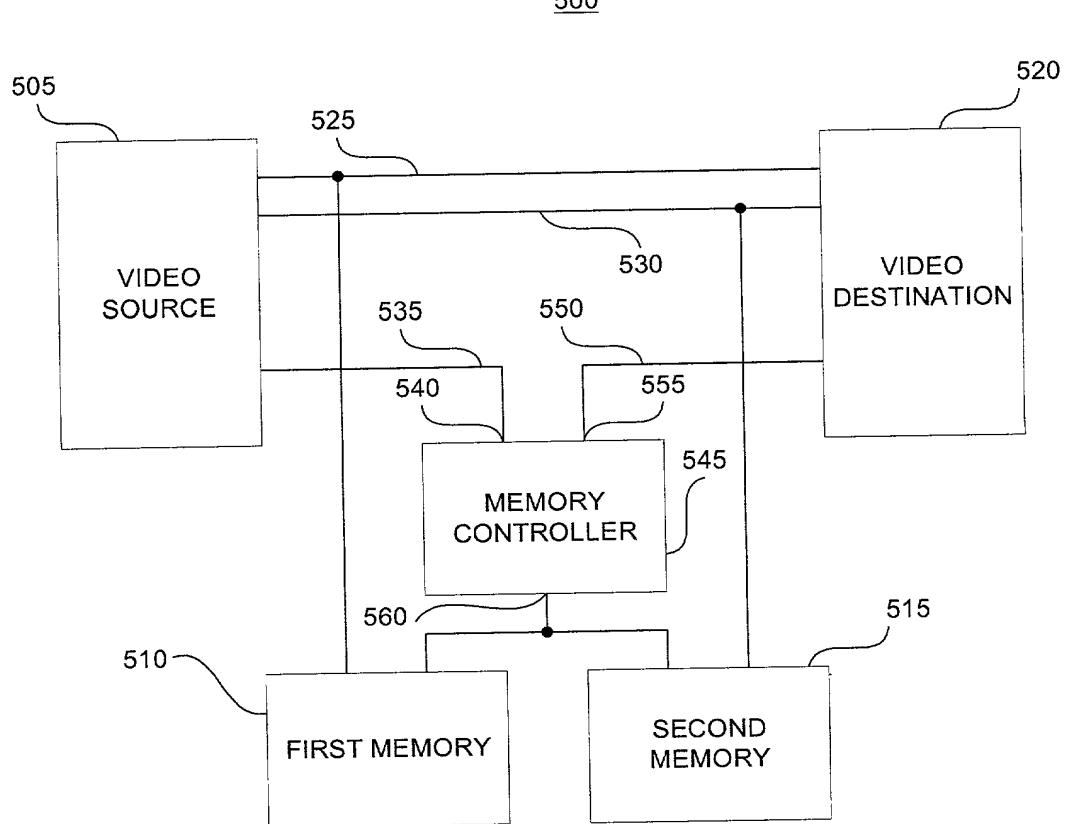


FIG. 5



43 65 4 52 4 3 0 1 1 6 0 2

FIG. 6A

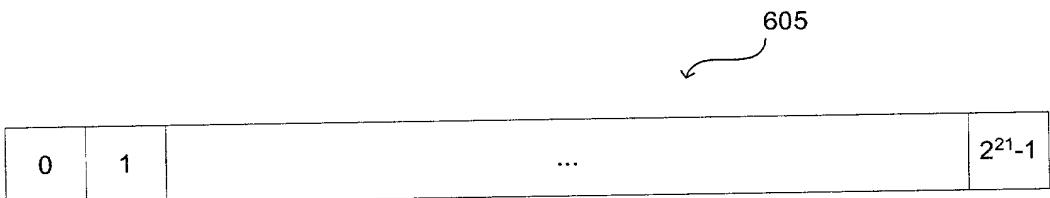


FIG. 6B

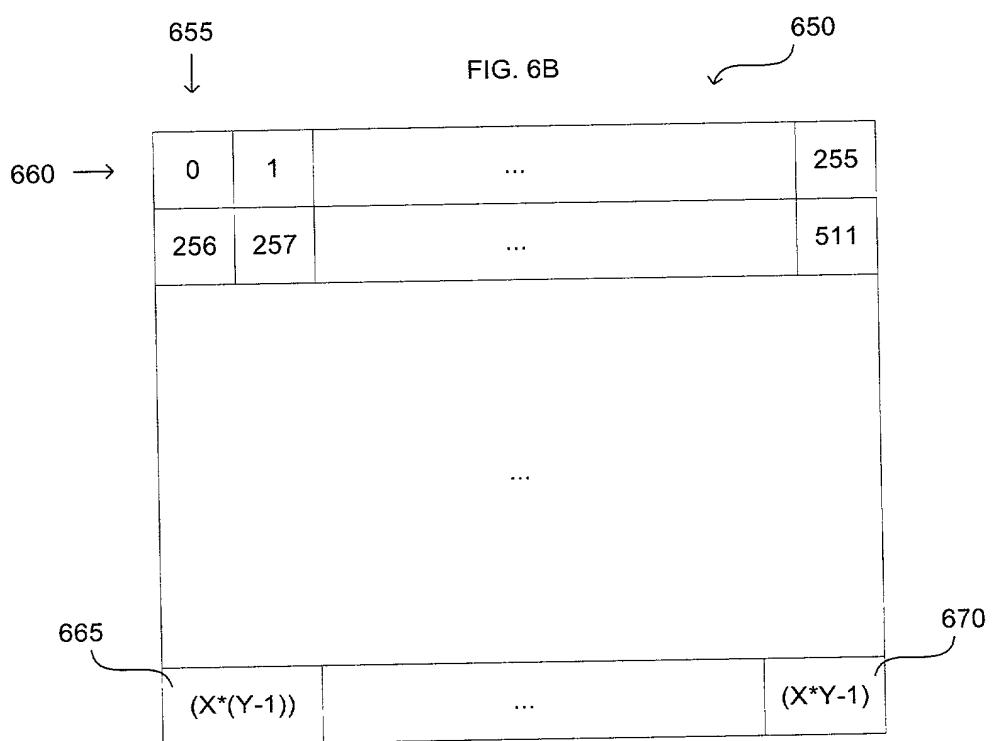


FIG. 6C

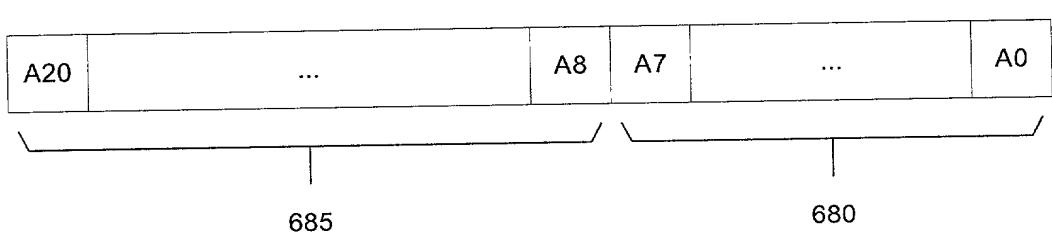


FIG. 7

The diagram shows a 16x16 grid of cells, each containing a unique integer value. The values range from 0 to 255, incrementing by 1 for each subsequent cell. The grid is labeled with its top-left cell as 0 and its bottom-right cell as 255. The grid is bounded by a thick black border. Four arrows point to specific cells:

- An arrow labeled 710 points to the cell containing the value 1.
- An arrow labeled 720 points to the cell containing the value 2.
- An arrow labeled 715 points to the cell containing the value 15.
- An arrow labeled 705 points to the cell containing the value 255.

0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31
32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47
48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63
64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79
80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95
96	97	98	99	100	101	102	103	104	105	106	107	108	109	110	111
112	113	114	115	116	117	118	119	120	121	122	123	124	125	126	127
128	129	130	131	132	133	134	135	136	137	138	139	140	141	142	143
144	145	146	147	148	149	150	151	152	153	154	155	156	157	158	159
160	161	162	163	164	165	166	167	168	169	170	171	172	173	174	175
176	177	178	179	180	181	182	183	184	185	186	187	188	189	190	191
192	193	194	195	196	197	198	199	200	201	202	203	204	205	206	207
208	209	210	211	212	213	214	215	216	217	218	219	220	221	222	223
224	225	226	227	228	229	230	231	232	233	234	235	236	237	238	239
240	241	242	243	244	245	246	247	248	249	250	251	252	253	254	255

FIG. 8A

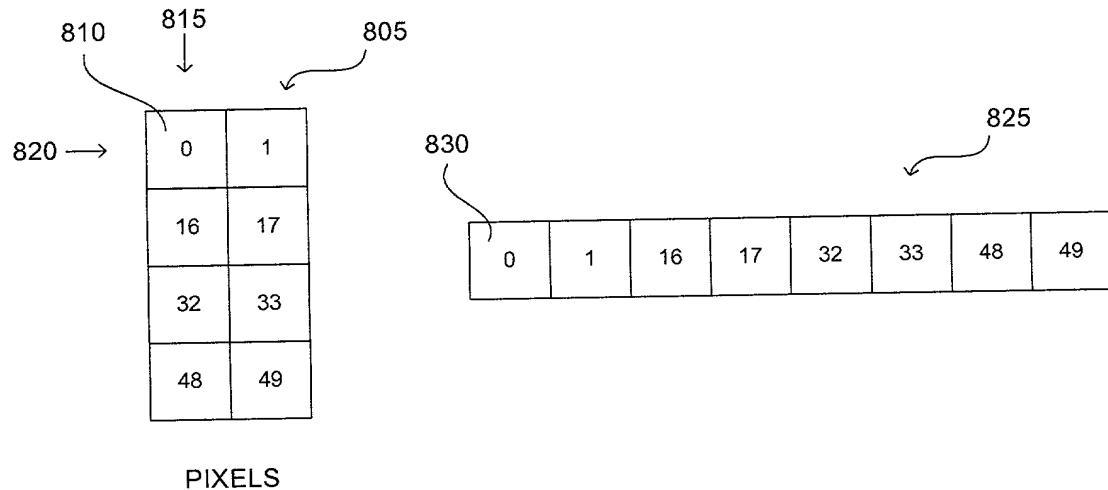


FIG. 8B

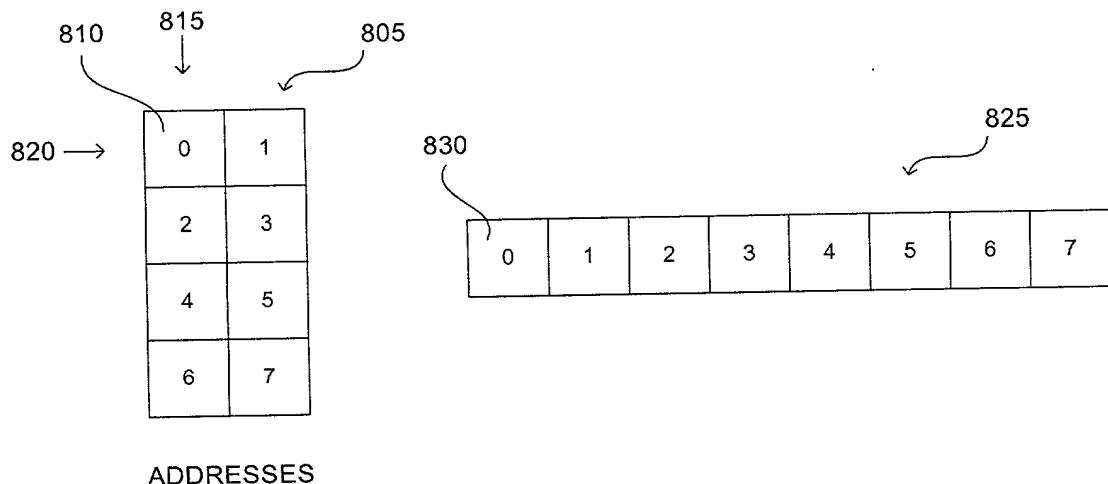


FIG. 9

The diagram illustrates a memory array structure. At the top left, the address 910 is shown above a row of four boxes labeled 0, 1, ..., 15. A bracket under the first two boxes is labeled 915, and an arrow points from 915 down to the first two boxes. To the right of the last box, another bracket is labeled 905, and an arrow points from 905 to the last box. Below this row, the address 920 is followed by an arrow pointing to a 4x4 grid of boxes. The grid contains the following data:

0	1	...	15
1920	1921	...	1935
...
28800	28801	...	28815

1000515441 0 1 2 3 4 5 6 7 8 9

FIG. 10

1000

1005 1010 1015 1020 1025 1030 1035 1040

PIXEL	FRAME ROW	FRAME COLUMN	PIXEL PAGE	PIXEL PAGE ROW	PIXEL PAGE COLUMN	MEMORY PAGE	MEMORY ADDRESS
0	0	0	0	0	0	0	0
1	0	1	0	0	1	0	1
15	0	15	0	0	15	0	15
16	0	16	1	0	0	1	256
31	0	31	1	0	15	1	271
1919	0	1919	119	0	15	119	30479
1920	1	0	0	1	0	0	16
1921	1	1	0	1	1	0	17
1935	1	15	0	1	15	0	31
1936	1	16	1	1	0	1	272

FIG. 11

0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31
32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47
48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63
64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79
80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95
96	97	98	99	100	101	102	103	104	105	106	107	108	109	110	111
112	113	114	115	116	117	118	119	120	121	122	123	124	125	126	127
128	129	130	131	132	133	134	135	136	137	138	139	140	141	142	143
144	145	146	147	148	149	150	151	152	153	154	155	156	157	158	159
160	161	162	163	164	165	166	167	168	169	170	171	172	173	174	175
176	177	178	179	180	181	182	183	184	185	186	187	188	189	190	191
192	193	194	195	196	197	198	199	200	201	202	203	204	205	206	207
208	209	210	211	212	213	214	215	216	217	218	219	220	221	222	223
224	225	226	227	228	229	230	231	232	233	234	235	236	237	238	239
240	241	242	243	244	245	246	247	248	249	250	251	252	253	254	255

FIG. 12

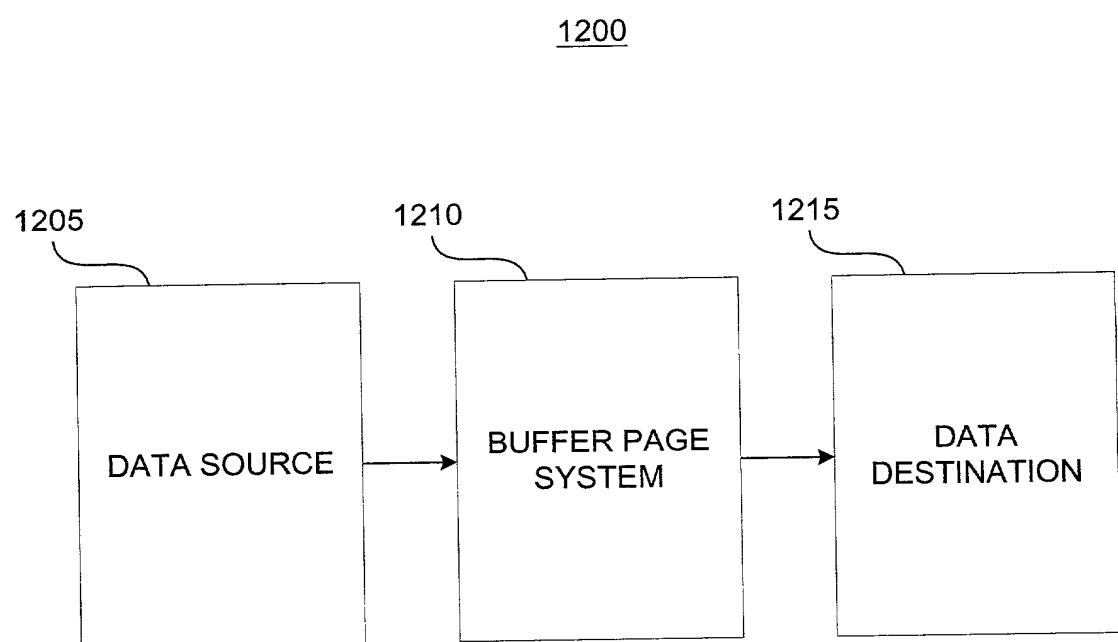
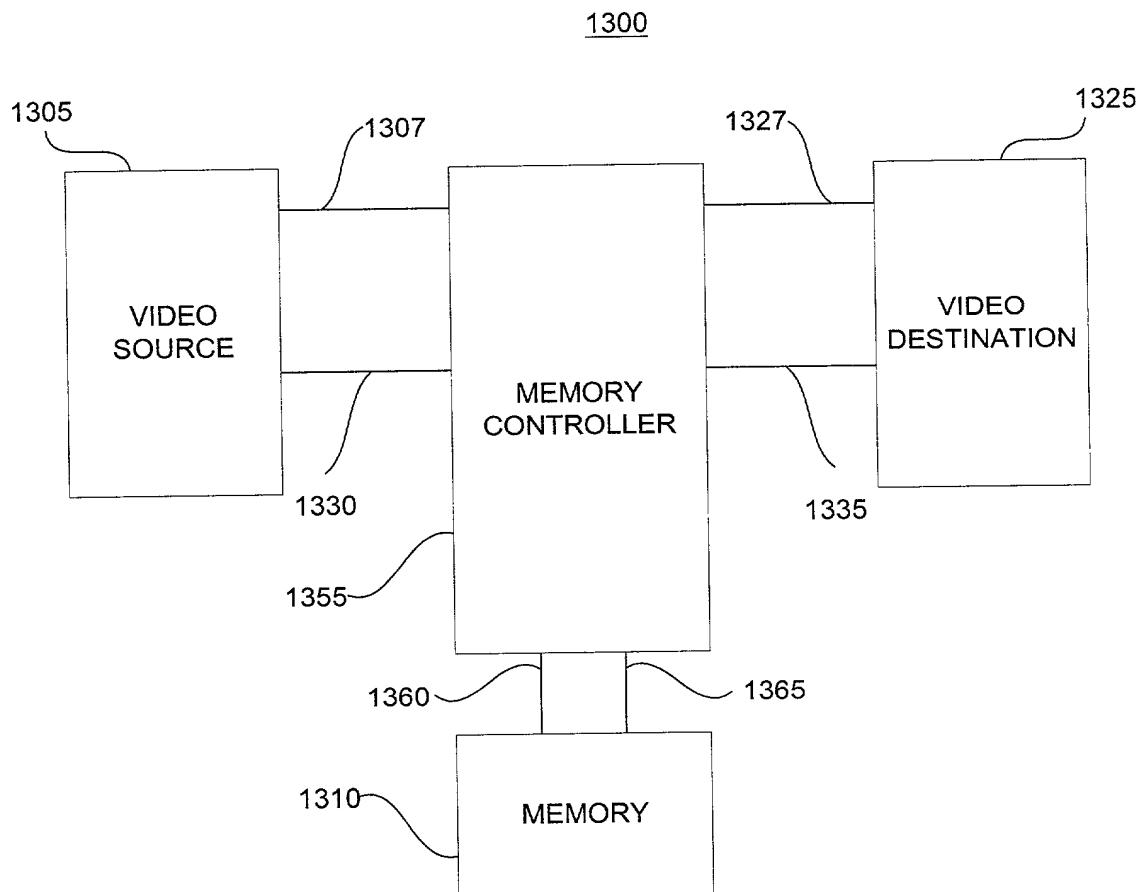


FIG. 13



20051640011602

FIG. 14

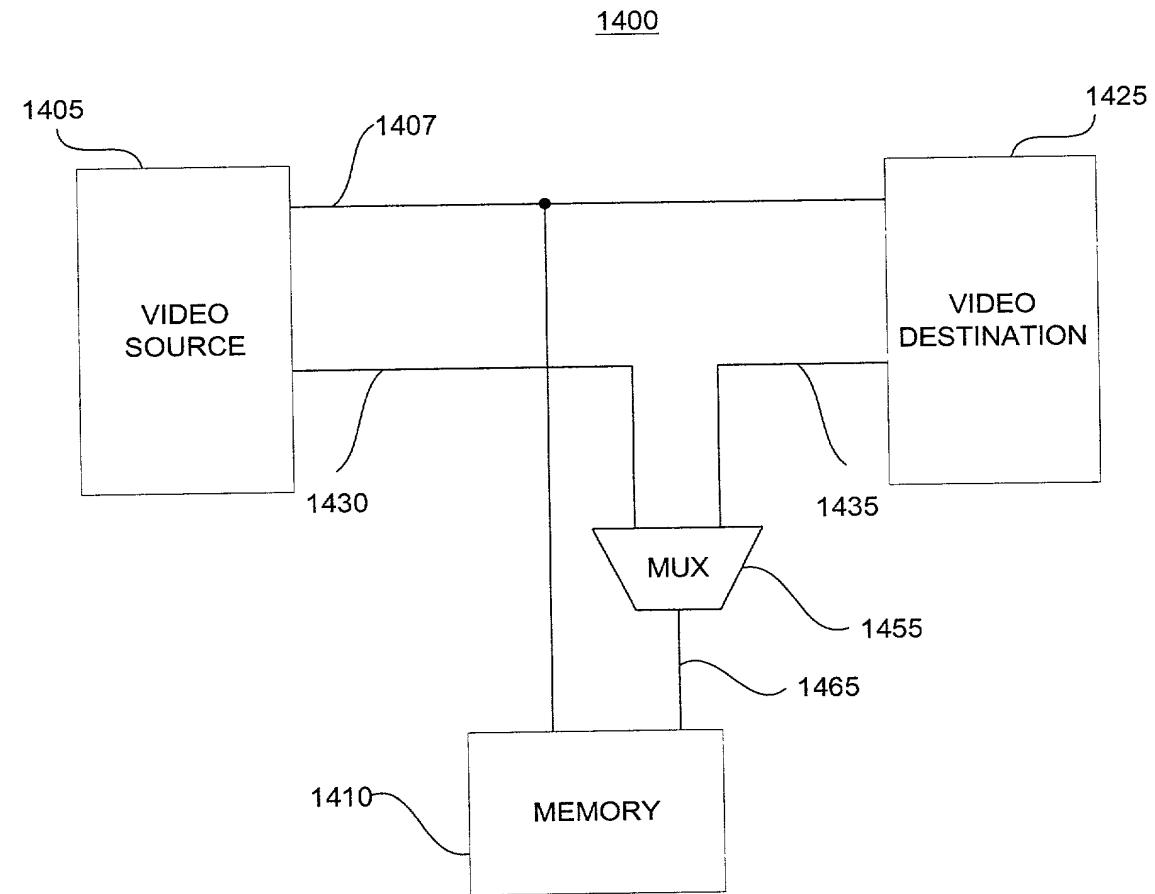


FIG. 15

1500

1505 1510 1515 1520 1525 1530 1535 1540

PIXEL	FRAME ROW	FRAME COLUMN	PIXEL PAGE	PIXEL PAGE ROW	PIXEL PAGE COLUMN	MEMORY PAGE	MEMORY ADDRESS
0	0	0	0	0	0	0	0
1	0	1	0	0	1	0	1
15	0	15	0	0	15	0	15
16	0	16	1	0	0	1	256
31	0	31	1	0	31	1	271
1919	0	1919	119	0	15	119	30479
1920	1	0	0	1	0	0	16
28800	15	0	0	15	0	0	240
28815	15	15	0	15	15	0	255
28831	15	31	1	15	15	1	511
30719	15	1919	119	15	15	119	30719
XXX	XXX	XXX	120	0	0	120	30720
30720	16	0	128	0	0	128	32768
2073599	1079	1919	8695	7	15	8695	2226047
XXX	XXX	XXX	8704	0	0	8704	2228224

FIG. 16

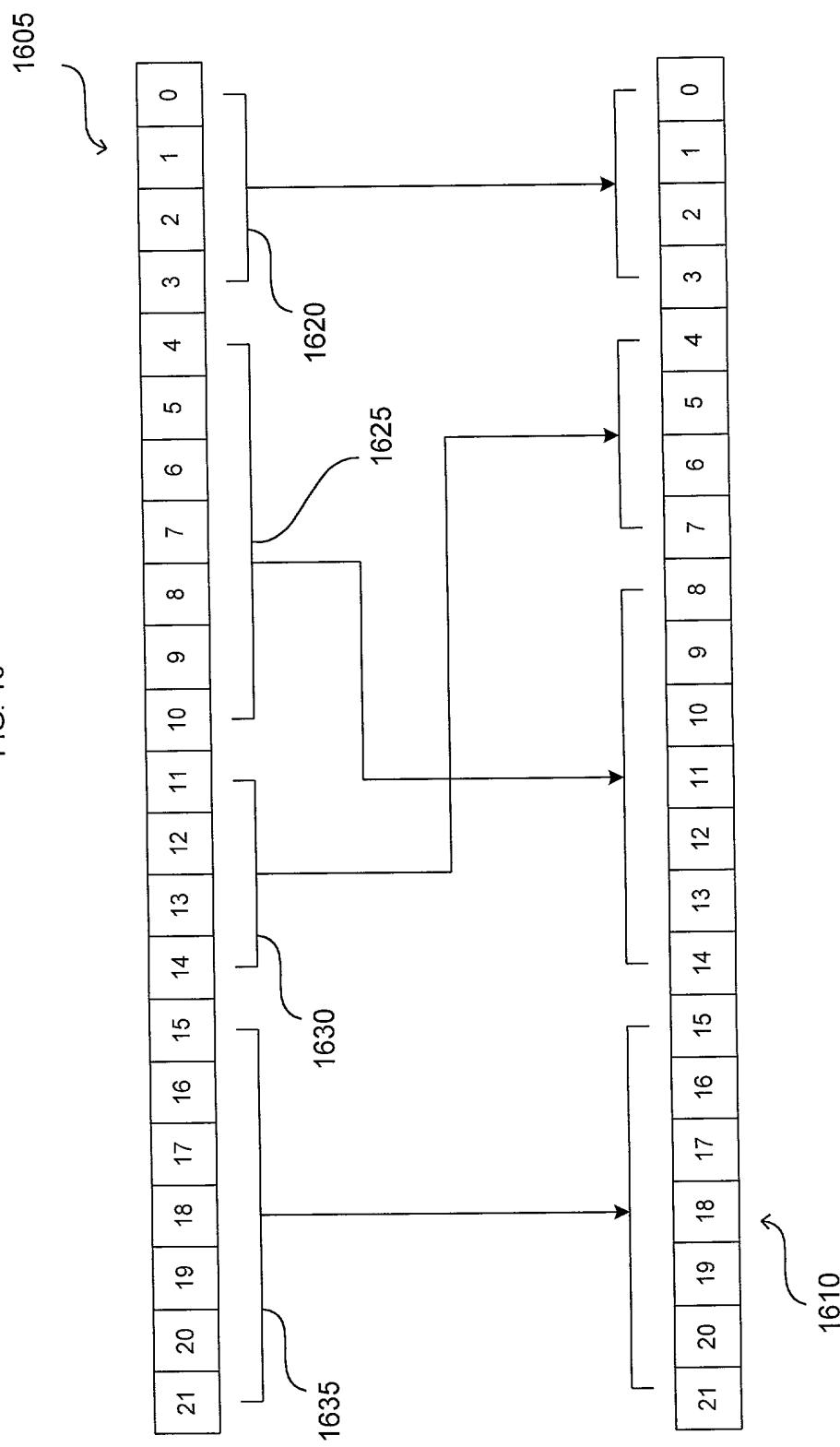


FIG. 17

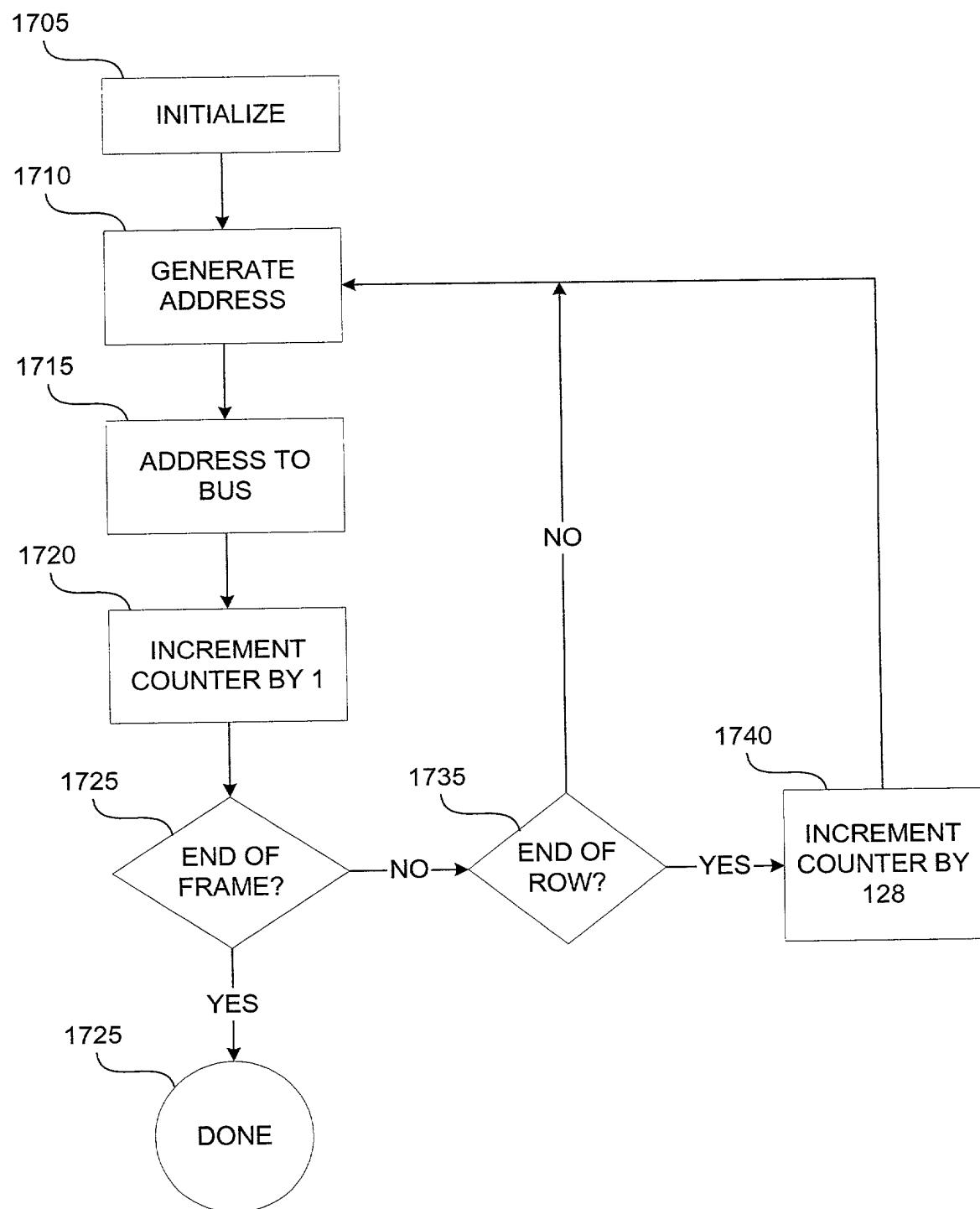
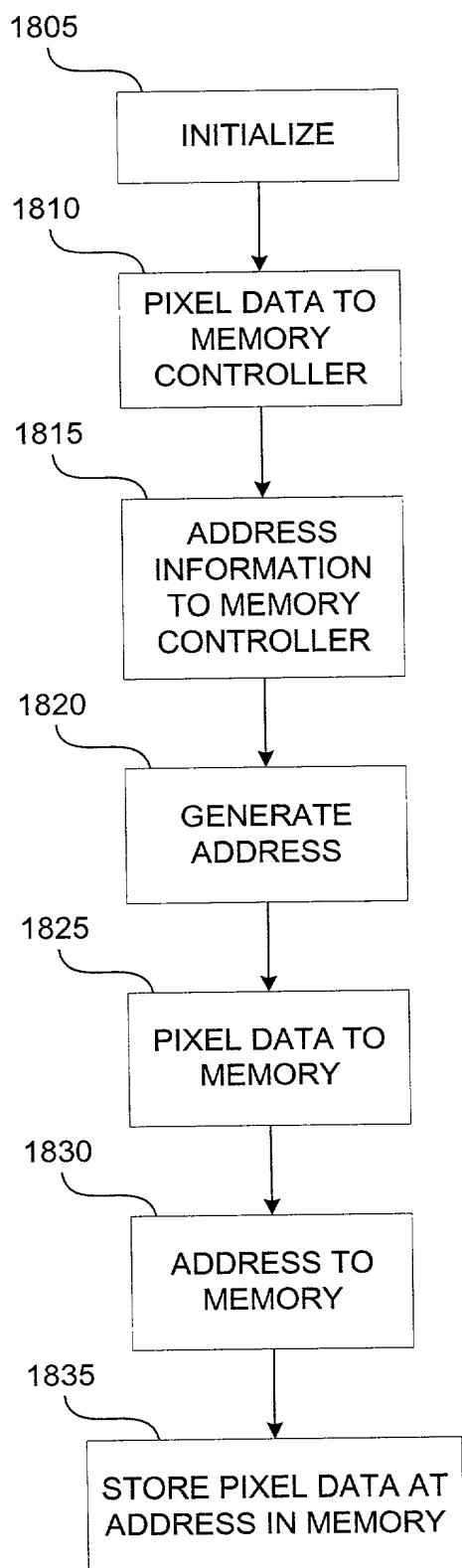


FIG. 18



20090315041011602

FIG. 19

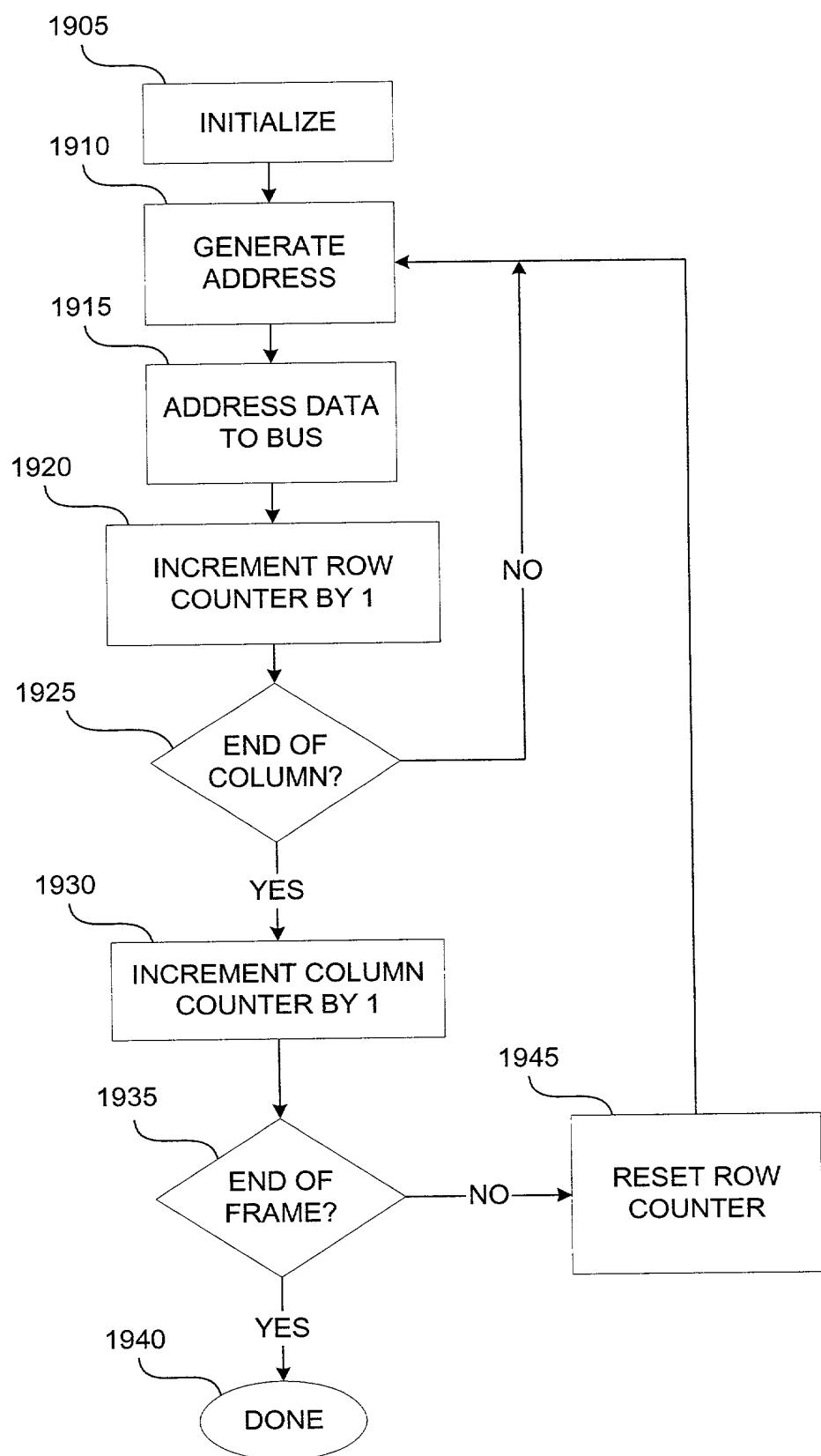
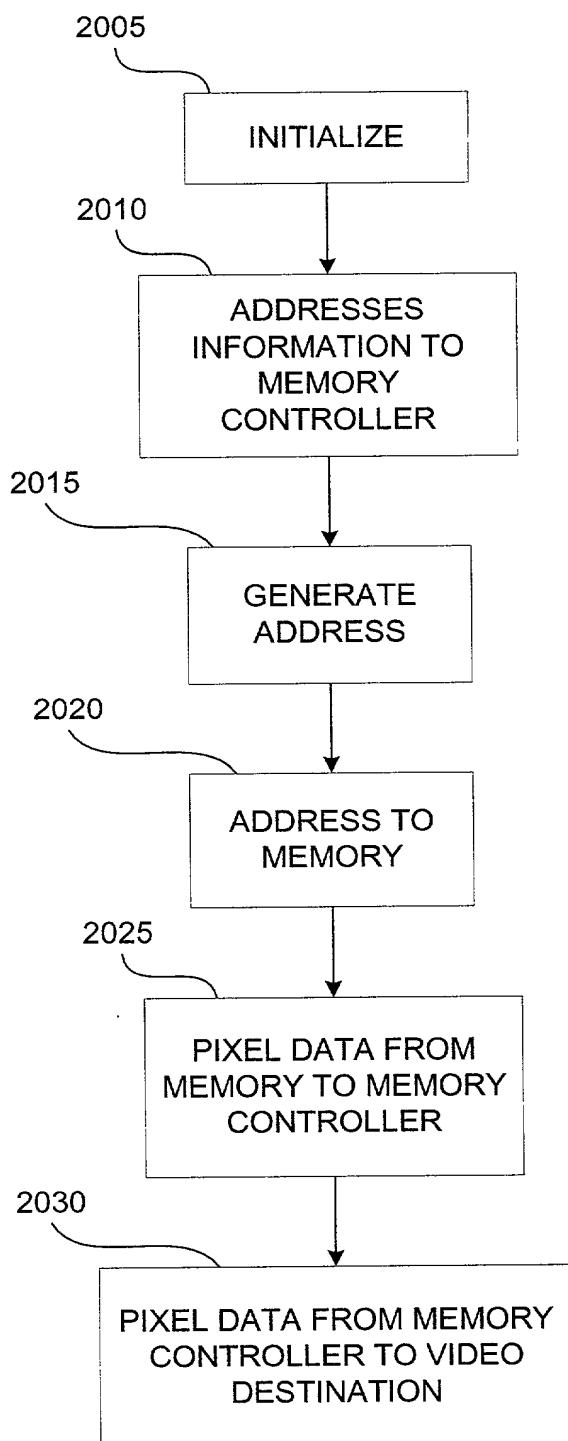


FIG. 20



4,600,545-1 - 03.1.16.02

FIG. 21

2100

PIXEL	FRAME ROW	FRAME COLUMN	PIXEL PAGE	PIXEL PAGE ROW	PIXEL PAGE COLUMN	MEMORY PAGE	MEMORY ADDRESS
0	0	0	0	0	0	0	0
1	0	1	0	0	1	0	1
15	0	15	0	0	15	0	15
16	0	16	1	0	16	1	256
31	0	31	1	0	31	1	271
1919	0	1919	119	0	15	119	30479
1920	1	0	0	1	0	0	16
28800	15	0	0	15	0	0	240
28815	15	15	0	31	15	0	255
28831	15	31	1	31	15	1	511
30719	15	1919	119	31	15	119	30719
30720	16	0	120	0	0	120	30720
30848	16	128	128	0	0	128	32768
2073599	1079	1919	8159	7	15	8159	2088831
XXX	XXX	XXX	8159	15	15	8159	2088959

100051531 00116622

FIG. 22

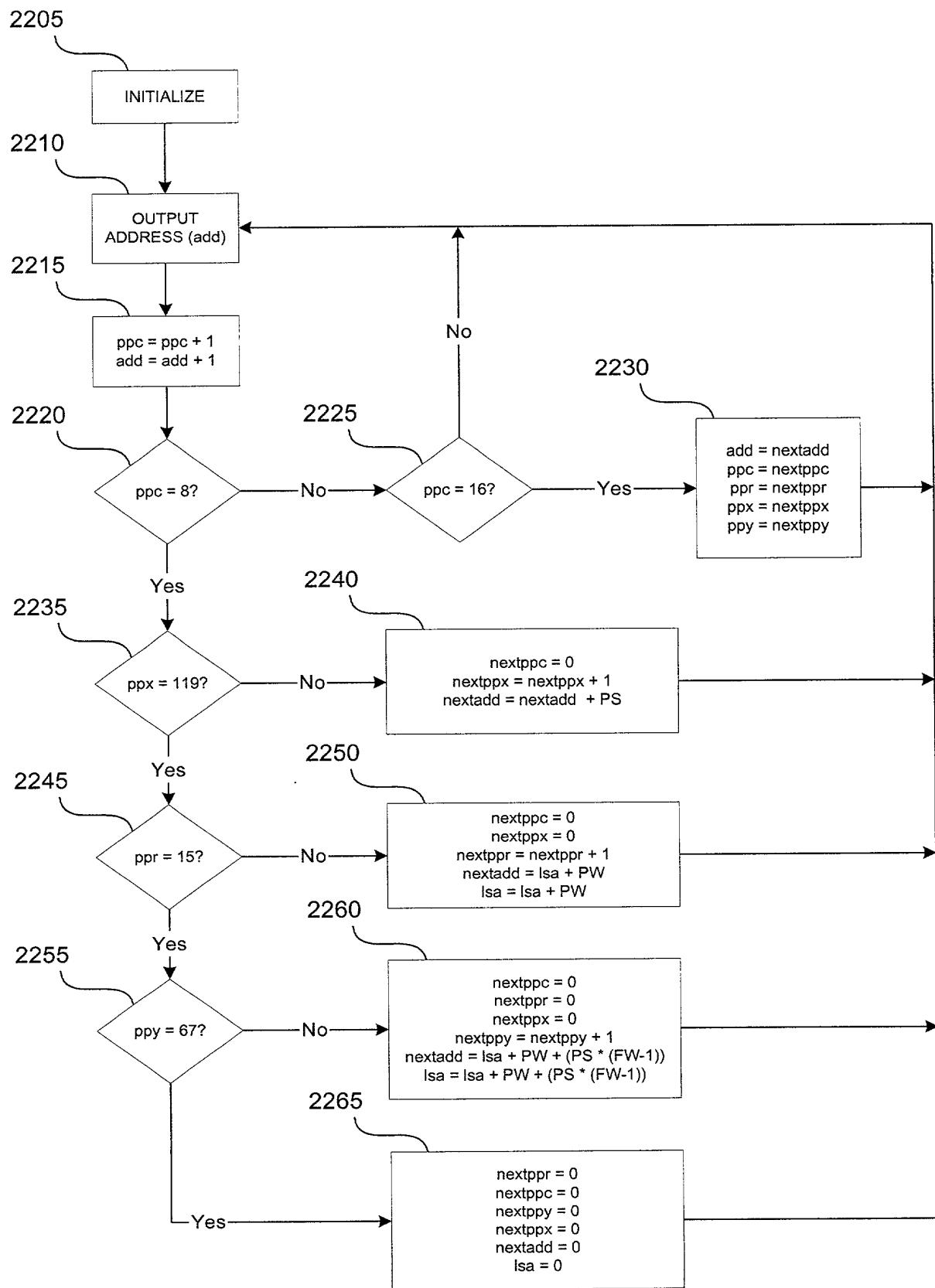
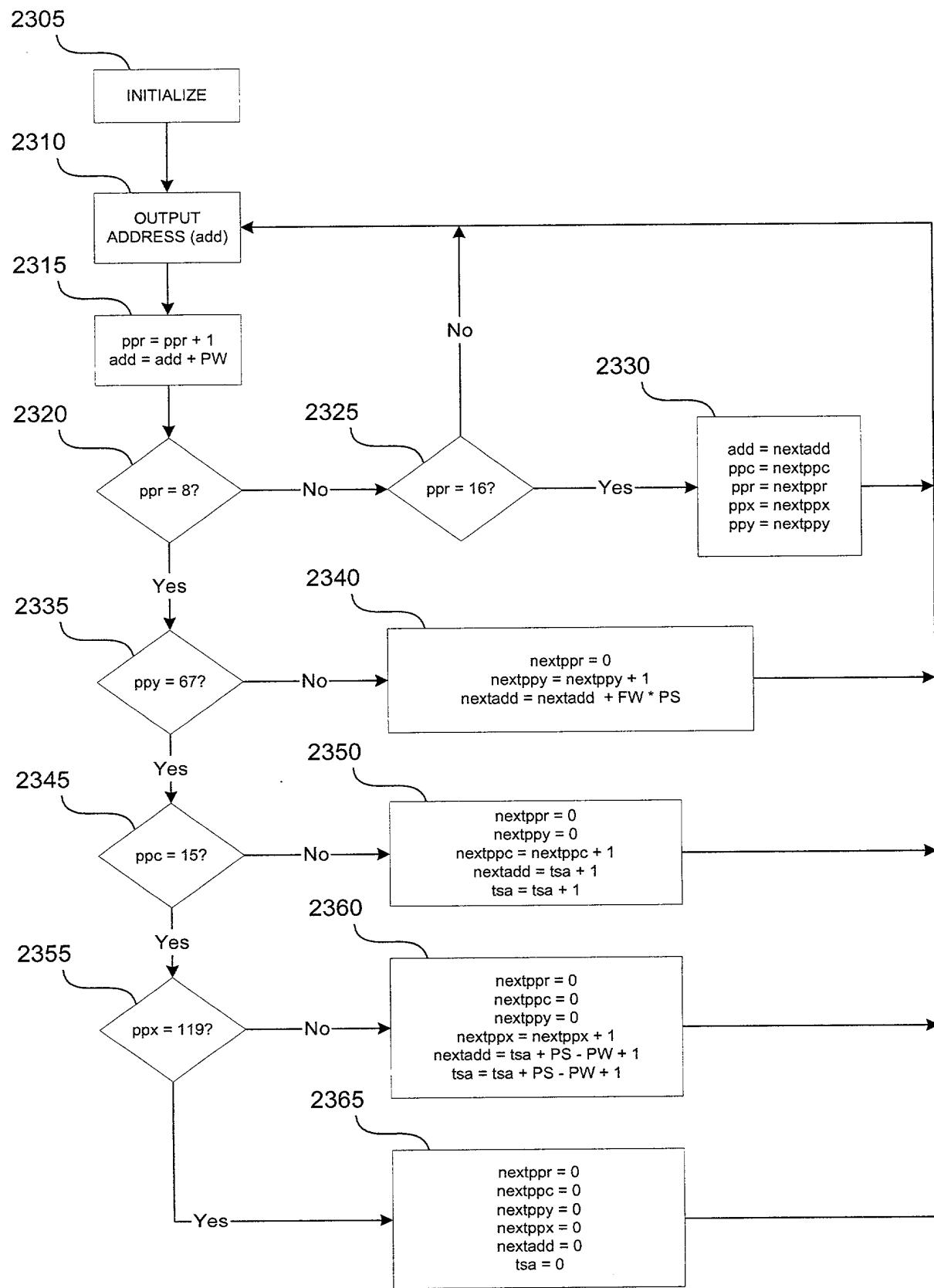


FIG. 23



1.0001100110011001

FIG. 24

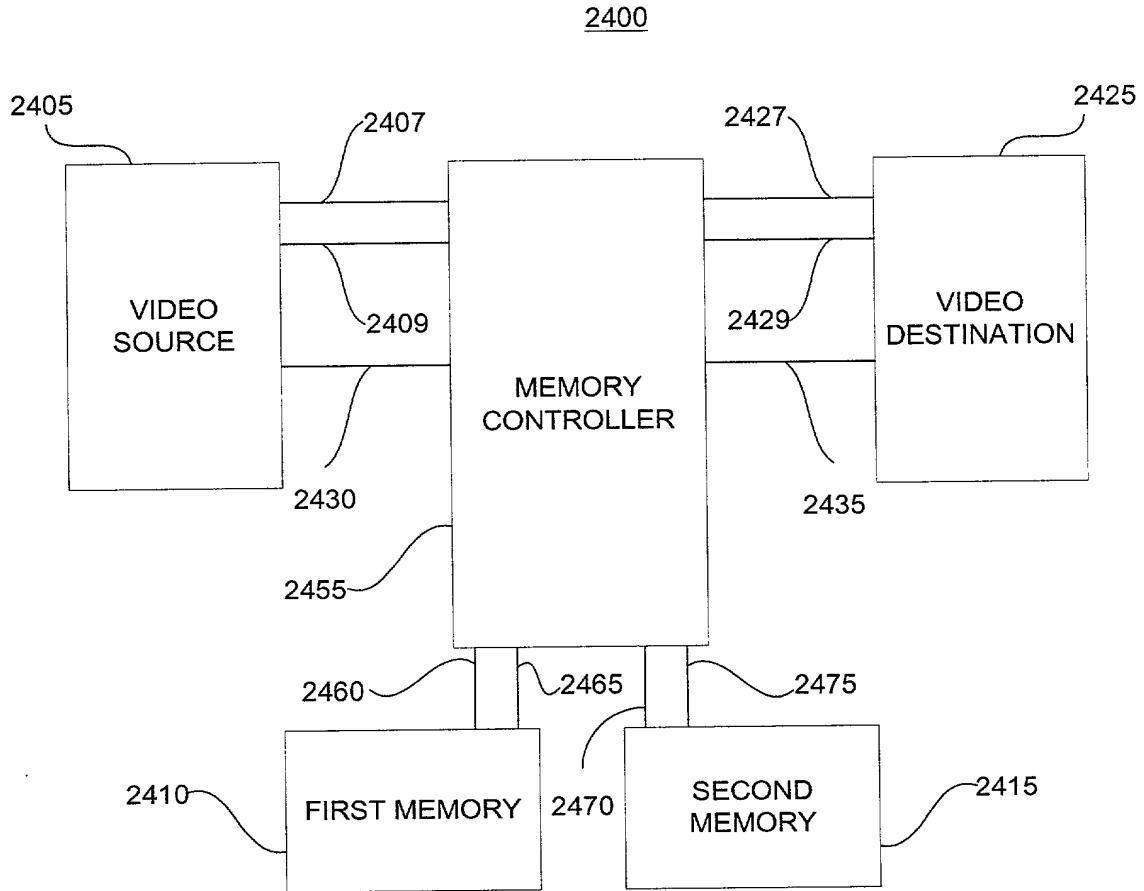


FIG. 25

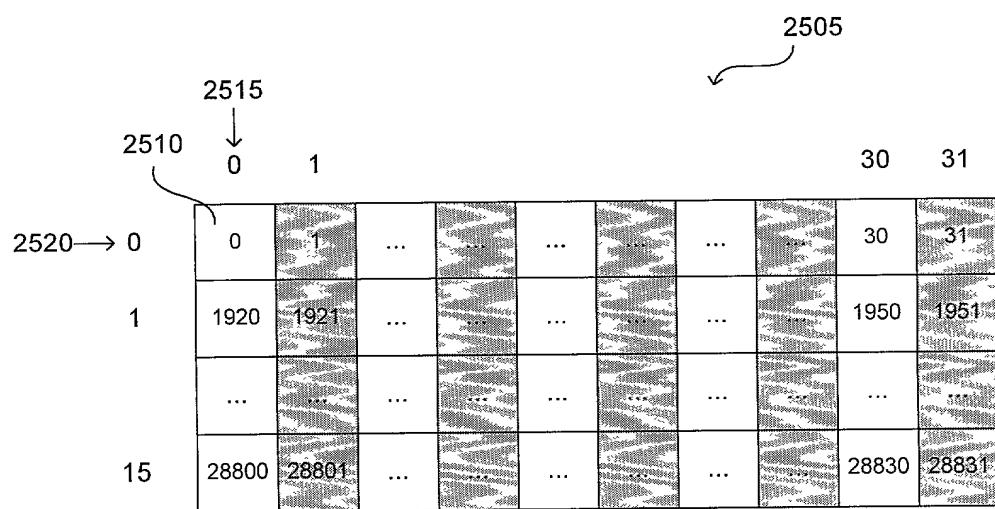


FIG. 26

2600

2605 2610 2615 2620 2625 2630 2635 2640 2645

PIXEL	FRAME ROW	FRAME COLUMN	PIXEL PAGE	PIXEL PAGE ROW	PIXEL PAGE COLUMN	MEMORY PAGE	MEMORY ADDRESS	MEMORY DEVICE
0	0	0	0	0	0	0	0	0
1	0	1	0	0	1	0	0	1
15	0	15	0	0	15	0	7	1
16	0	16	0	0	16	0	8	0
31	0	31	0	0	31	0	15	1
1919	0	1919	59	0	31	59	15118	1
1920	1	0	0	1	0	0	16	0
28800	15	0	0	15	0	0	240	0
28815	15	15	0	15	15	0	247	1
28831	15	31	0	15	31	0	255	1
30719	15	1919	59	15	31	59	15359	1
XXX	XXX	XXX	60	0	0	60	15360	0
30720	16	0	64	0	0	64	16384	0
2073599	1079	1919	4347	7	31	4347	1112959	1
XXX	XXX	XXX	4352	0	0	4352	1114112	0

160054341 0116002

FIG. 27

2705

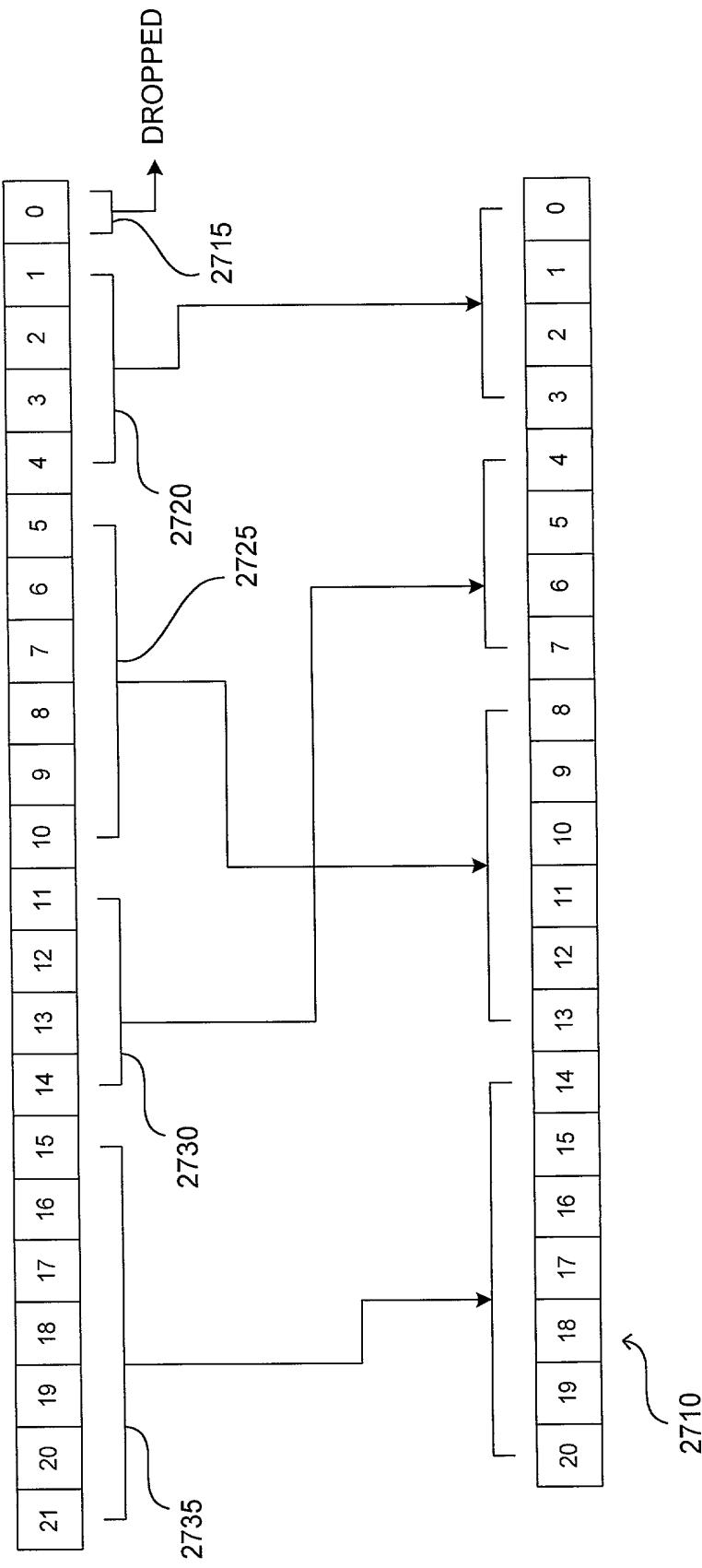


FIG. 28

2800

PIXEL	FRAME ROW	FRAME COLUMN	PIXEL PAGE	PIXEL PAGE ROW	PIXEL PAGE COLUMN	MEMORY PAGE	MEMORY ADDRESS	MEMORY DEVICE
0	0	0	0	0	0	0	0	0
1	0	1	0	0	1	0	0	1
15	0	15	0	0	15	0	7	1
16	0	16	0	0	16	0	8	0
31	0	31	0	0	31	0	15	1
1919	0	1919	59	0	31	59	15119	1
1920	1	0	0	1	0	0	16	0
28800	15	0	0	15	0	0	240	0
28815	15	15	0	15	15	0	247	1
28831	15	31	0	15	31	0	255	1
30719	15	1919	59	15	31	59	15359	1
30720	16	0	60	0	0	60	15360	0
30848	16	128	64	0	0	64	16384	0
2073599	1079	1919	4079	7	31	4079	1044351	1
XXX	XXX	XXX	4079	15	31	4079	1044479	1

FIG. 29

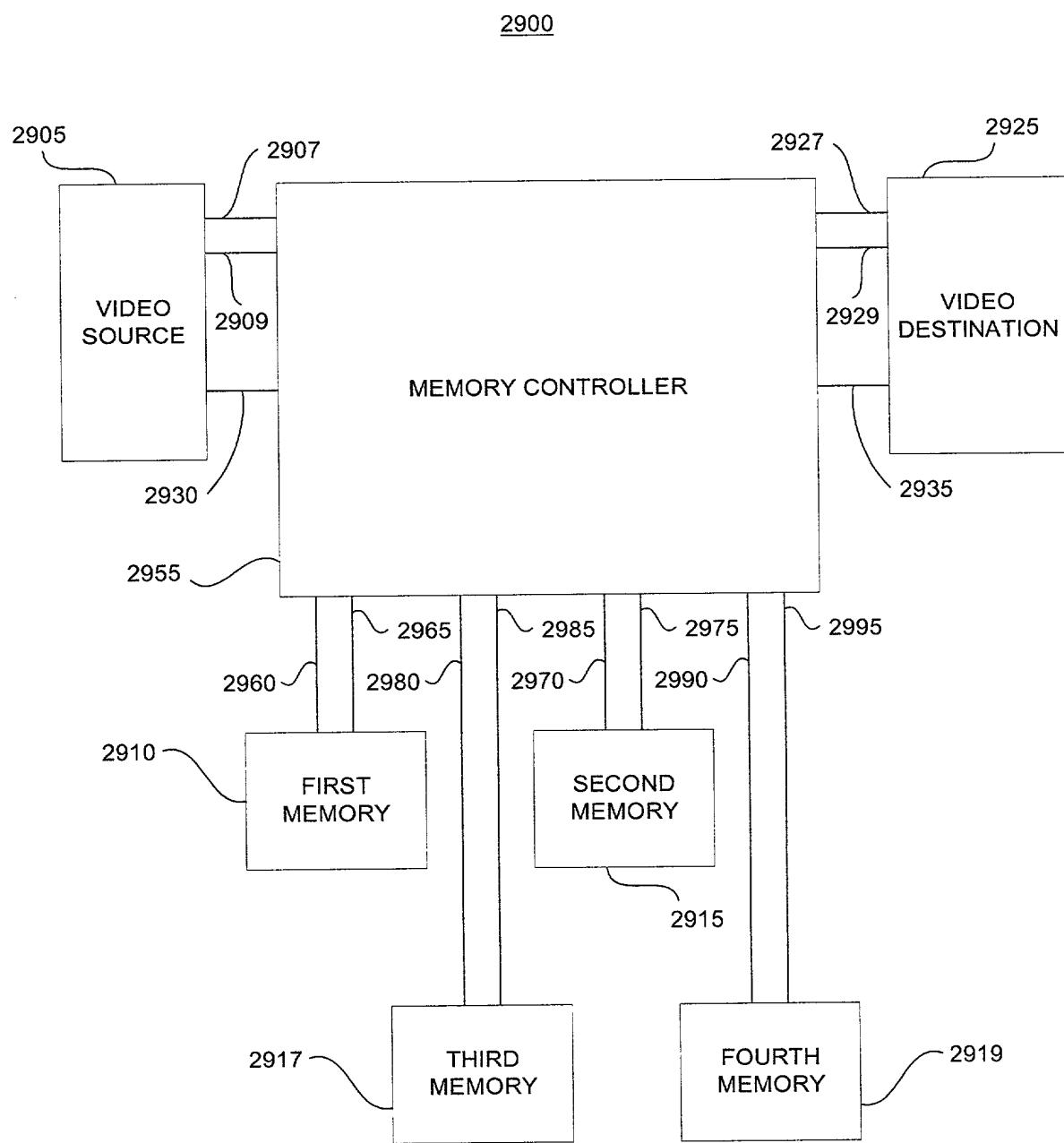


FIG. 30

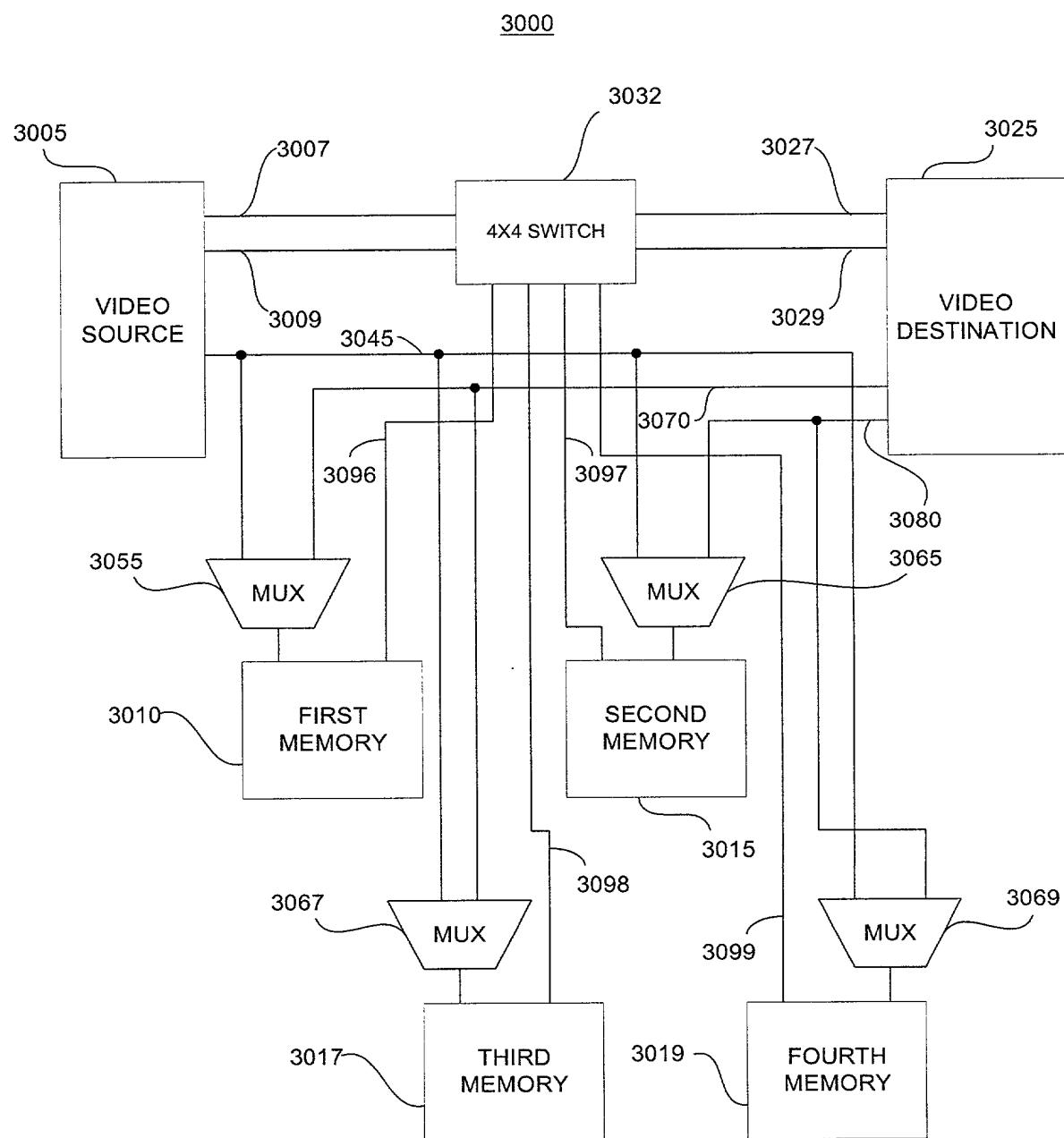
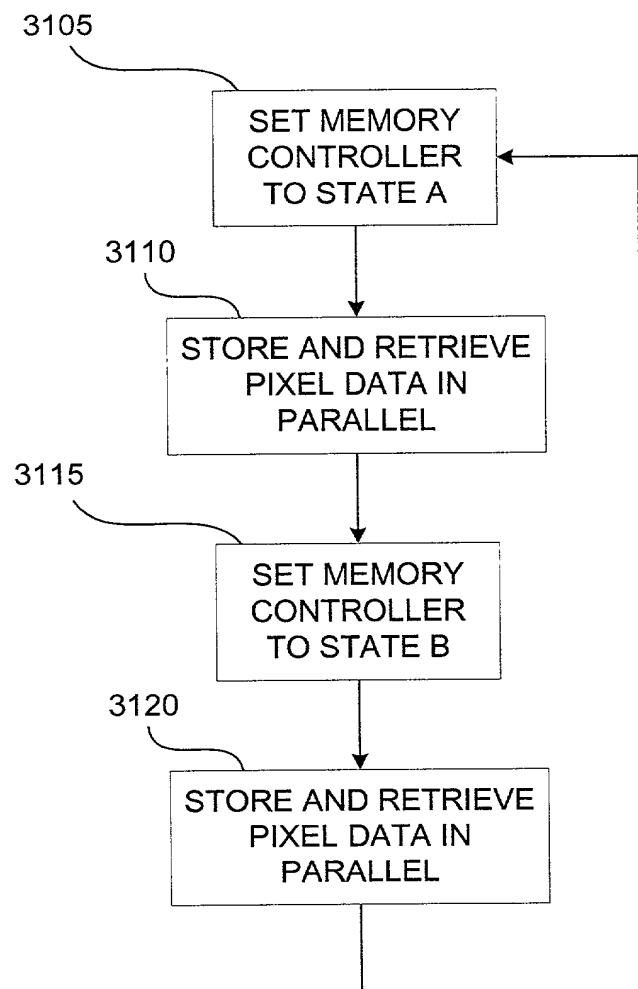


FIG. 31



43010584.5544.31 0 3 0.1602

FIG. 32

